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*Admitted only in Maryland
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February 22, 2005

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Commissioner for Patents
PO Box 1450
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Mail Stop Amendment

Re: U.S. Utility Patent Application
Appl. No. 10/643,957; Filed: August 20, 2003
For: **High Voltage Power Management Unit Architecture In CMOS Process**
Inventors: Chen *et al.*
Our Ref: 1875.4170000/JTH/GSB

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Reply Under 37 C.F.R. § 1.111;
2. Declaration Under 37 C.F.R. § 1.131 by George S. Bardmesser (with Exhibits A and B);
3. Declaration of Chun-Ying Chen and Hsiang-bin Lee Under 37 C.F.R. § 1.131 (with Exhibits A and B); and
4. One (1) return postcard.

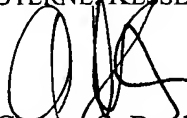
It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

Commissioner for Patents
February 22, 2005
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The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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JTH/GSB/lam
Enclosures

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